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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/524,571	02/15/2005	Igor Ivanovich Blednov	NL 020752	8310
24737	7590	11/17/2005	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001 BRIARCLIFF MANOR, NY 10510			LEE, JOHN D	
			ART UNIT	PAPER NUMBER
			2874	

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/524,571

Applicant(s)

BLEDNOV, IGOR IVANOVICH

Examiner

John D. Lee

Art Unit

2874

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 03 November 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-10 and 12-17 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10 and 12-17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 15 February 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 1005.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

Art Unit: 2874

Applicant's communication filed on November 3, 2005, has been carefully studied by the Examiner. The arguments advanced therein, considered together with the amendments made to the claims, are persuasive and the rejections based upon prior art made of record in the previous Office action are withdrawn. The amendments have also obviated the previously indicated objections to the claims, and those objections are withdrawn. In view of further search, however, and the consequent discovery of a previously uncited prior art document, new rejections are set forth below. This action is **not** made final.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 8-10, 12, and 14 are rejected under 35 U.S.C. § 102(e) as being clearly anticipated by U.S. Patent 6,731,173 to Thompson (newly cited). Thompson discloses a Doherty power amplifier circuit having an input terminal **118** and an output terminal **120** comprising: a carrier transistor amplifying stage **110**; a peak transistor amplifying stage **112**; a first input line **228** connecting the input terminal to an input of the carrier amplifier; a second input line **230** connecting the input terminal to an input of the peak amplifier; a first output line (unnumbered) connecting the output terminal to an output of the carrier amplifier; and a second output line (unnumbered) connecting the output

Art Unit: 2874

terminal to an output of the peak amplifier. As seen in Figures 1 and 2, Thompson also discloses a Doherty bias control circuit connected to the carrier transistor and the peak transistor for providing desired dynamic controlling of amplification class parameters of the transistors (column 2, lines 52-64). Regarding claim 8, it is noted that the Thompson carrier transistor and peak transistor have individual transconductance parameters and threshold voltage values (regardless of what numerical value (even zero) that the parameters and values may assume). Regarding claims 9 and 10, the Thompson carrier transistor and peak transistor are each connected to a "compensation" circuit or "impedance transformation" circuit 114. Regarding claims 12 and 14, a "support structure" for supporting all the circuit elements discussed above is inherently present in the Thompson device, since the circuit elements must be arranged as shown and cannot be unsupported.

The following is a quotation of 35 U.S.C. § 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 2-7, 13, and 15-17 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent 6,731,173 to Thompson (newly cited). As explained above, Thompson discloses the basic claimed Doherty power amplifier circuit and associated dynamic control circuit. Thompson does not explicitly disclose, in the first and second input lines or in the first and second output lines, the presence of an inductor and/or a capacitor or serial circuits and/or parallel circuits of at least one

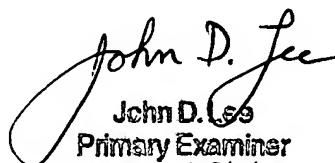
Art Unit: 2874

capacitance and/or at least one inductance. These fundamental circuit elements would have been obvious components, however, of the "Input Network" 116 and the "Output Network" 114, because of the disclosed functioning of these networks in the reference. Regarding claims 16 and 17, the following is noted. In discussing the various lines and inductances, Thompson does not use the word "wires". Since this is the most common implementation known in the art, it would have been obvious to the person of ordinary skill to have used "bond wires" of appropriate length for the inductances and input lines of the Thompson amplifier circuit.

The prior art document cited by applicant in the Information Disclosure Statement filed on October 26, 2005, has been considered and made of record. Note the attached initialed copy of form PTO-1449.

Applicant's arguments with respect to claims 1-10 and 12-17 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning the merits of this communication should be directed to Examiner John D. Lee at telephone number (571) 272-2351. The Examiner's normal work schedule is Tuesday through Friday, 6:30 AM to 5:00 PM. Any inquiry of a general or clerical nature (i.e. a request for a missing form or paper, etc.) should be directed to the Technology Center 2800 receptionist at telephone number (571) 272-1562, to the technical support staff supervisor (Team 8) at telephone number (571) 272-1564, or to the Technology Center 2800 Customer Service Office at telephone number (571) 272-1626.

  
John D. Lee  
Primary Examiner  
GAU 2874